



FT1A Series Smart AXIS - 12 I/O

Key Features

- Available in 100-240V AC and 24V DC power
- Available with/without embedded LCD
- 10 Amp Relay contacts
- USB Mini-B Programming Port
- Embedded Real Time Clock
- Embedded 2-pt analog inputs (0-10V DC, 10-bit, DC power)
- Integrated 4 x 100KHz high-speed counters



General Specifications

Part Numbers	FT1A-H12RA	FT1A-B12RA	FT1A-H12RC	FT1A-B12RC	
Appearance					
LCD Screen	Yes	N/A	Yes	N/A	
Operating Temperature	0 to +55°C (operating ambient temperature)				
Storage Temperature	-25 to +70°C (no freezing)				
Rated Power Voltage	24V DC		100 to 240V AC		
Allowable Voltage Range	20.4 to 28.8V DC (Including ripple voltage)		85 to 264V AC		
Rated Power Frequency	-		50/60Hz (47 to 63Hz)		
Maximum Power Consumption	4.3W		18VA		
Weight	Approx. 190g		Approx. 230g		



Function Specifications

Part Numbers		FT1A-H12RA, B12RA	FT1A-H12RC, B12RC	
Program Capacity Note 1		12,000 bytes (3,000 steps)		
	Points	8		
Input	Digital Input (Terminal No.)	6 (10 to 15)	8 (I0 to I7)	
	Shared Analog Input (Terminal No.)	2 (16, 17)	_	
	Output Points	4		
	10A Relay Output (Terminal No.)	4 (Q0 to Q3)		
	2A Relay Output (Terminal No.)	_		
	Transistor Output (Terminal No.)	_		
User Program Storage		Flash ROM (10,000 rewriting life)		
	RAM	Backup data: Internal relay, shift register, counter current value, data register ^{Note 2} , clock data (year, month, and day)		
	Backup Duration	Approx. 30 days (typical) at 25°C after backup battery fully charge		
Backup Function	Battery	Lithium		
	Charging Time	Approx. 15 hours for charging from 0% to 90% of full charge		
	Battery Life	5 years		
	Replaceability	Not possible		
Clock Function Note 3		Clock accuracy: ±30 sec/month (typical) at 25°C		
Control System		Stored program system		

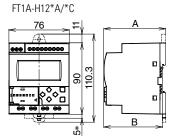
Specifications con't

Basic Instructions 42 Advanced Instructions 99 Processing Time Basic Instruction 99 END Processing END Processing 0.95ms (1000 steps) Internal Relay END Processing 640µs Shift Register 128 128 Data Register 400m 400m			
Advanced Instructions 99 Processing Time Basic Instruction 0.95ms (1000 steps) END Processing 640µs 1024 Internal Relay 1024 128			
Processing Time END Processing 640µs Internal Relay 1024 Shift Register 128			
END Processing 640µs Internal Relay 1024 Shift Register 128	0.95ms (1000 steps)		
Shift Register 128	640µs		
-	1024		
Data Begister 400m	128		
	400m		
Counter (adding, reversible) 100	100		
Timer (1-sec, 100ms, 10ms, 1ms) 100	100		
Input Filter Without filter, 3 to 15ms (selectable in increment	Without filter, 3 to 15ms (selectable in increments of 1ms)		
Catch Input/Interrupt Input Input Points 4	4		
	Keep data, Power failure, Clock error, Watchdog timer, Timer/counter preset value change error, User program syntax, User program execution, System error, Memory cartridge transfer error		
Points Total 4 points	_		
Hisk speed Counter Frequency Single/two-phase selectable: 100kHz (2 points) , Single-pha	ase: 100kHz (2 points)		
High-speed Counter Counting Range 0 to 4,294,967,295 (32 bit)	0 to 4,294,967,295 (32 bit)		
Operation Mode Rotary encoder mode and adding counter n	node		
Pulse Output (Maximum frequency: 100kHz) Points –			
Pulse Output (Maximum frequency: 5kHz) Points –			
Points (Terminal No.) 2 (16, 17)	_		
Analog Voltage Input voltage Range 0 to 10V DC			
Digital Resolution 0 to 1000			
Points 1	1		
USB Port USB Standard USB 2.0	USB 2.0		
Connector Mini-B type			
Expansion Communication Ports –	_		
Ethernet Port –	_		
Memory Cartridge Connectors 1	1		
SD Memory Card Slots –	_		

Step is equivalent to 4 bytes.
Among data registers D0 to D1999, only D0 to D999 are backed up.
Set the calendar/clock using the clock function in WindLDR.

Dimensions (mm)

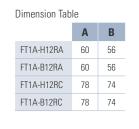
With LCD



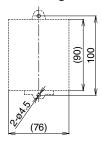


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Mounting Hole Layout



Without LCD